ABHISHEK SAURAV

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OBJECTIVE

- To work in competitive Assignments that shall yield job satisfaction, steady placed organizational elevation and societal contribution which I consider three pillars.

EDUCATION DETAILS

Degree	School/College	University/Board	CGPA/ Percentage	Year of Passing
B.Tech (E.C.E)	K. L. University	Deemed University	7.76 CGPA	2019
12th	Satyam International	CBSE	65%	2015
10th	R.P.S Public School	CBSE	9.0 CGPA	2013

SKILLS & ABILITIES

- Tools   : Riviera Pro, CST, HFSS, Cisco, NI Lab View, Matlab
- Languages   : Verilog, System Verilog
- Verification Methodology   : UVM
- Software Skills   : C, JAVA
- Platform   : Windows, Linux, Arduino
- Scripting   : Perl,Tcl
- Protocols   : APB

CERTIFICATION

- Certified in **Salesforce Marketing Cloud**.

- Being trained in **VLSI Design and verification** for duration of 6 months.

PROJECTS DURING CERTIFICATION

- **PROJECT 1:**

  Title   : FIFO (First In First Out)
  Duration   : 2 Weeks
  Language   : Verilog and System Verilog
  Tool Used   : Rivera Pro

Description: A memory in which the data word that is written in first comes out first is a first -in first-out memory. In digital engineering, there is a constantly recurring problem of synchronizing two systems that work at different frequencies. Concurrent read/write FIFO's can also handle the data exchange between two systems of different frequencies.

- **PROJECT 2:**

  Title   : Verification of AMBA-Advanced Peripheral Bus(APB) Protocol
  Duration   : 2 Weeks
  Language   : System Verilog
  Methodology: UVM
  Tool Used   : Rivera Pro

Description: AMBA APB addresses the requirements of low performance synthesizable designs. It is a bus interface that supports a single bus master and multiple slaves and provides low-bandwidth operation.

ACADEMIC PROJECTS

PROJECT 1:

Title   **:** Design and Analysis of Triple Band Antenna for UWB Application
Tool Used **:** CST.

- Design analysis and Optimization of the Antenna Design.
- Proposed antenna gives good simulation results in terms of return loss, VSWR and Radiation Pattern.
- Improvement in the impedance bandwidth and generates the omni-directional radiation pattern.

PROJECT 2:

Title   : Number plate reorganization using MATLAB.
Tool Used   : MATLAB.

- **Template Creation:** This is used to call the saved image of alpha numeric and then save them as a new template in MATLAB memory.
- **Letter Detection:** Reads the characters from the input image and find the highest matched corresponding alphanumeric.
- **Plate Detection**: Process the image and then call the above two m-files to detect the number.

PROJECT 3:

Title   : Network Classification
Technologies Used   : Cisco.

- Specify the matching criteria used to define a traffic class
- Create a traffic policy.
- Apply the policy actions specified in the policy map to an interface, sub interface, or ATM permanent virtual circuit (PVC).

WORK EXPERIANCE

•   Worked as BDA in BYJU's (Think and Learn Pvt. Ltd.) for 3 months.

DECLARATION

I, Abhishek here by confirm that the information given above is true to the best of my knowledge.

Date   :

Place :   **Signature**