**Narasimha N**

No. 41/1A, 1st block, 1st cross, Sai baba mandira road, Thyagarajanagar, Bangalore - 560028

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Career Objective

To work in a challenging environment in the embedded systems design domain using all my skills & knowledge and an opportunity for continuous learning.

Professional Training

**Advanced Diploma in Embedded Systems Design** at **RV-VLSI Design Centre**, Bangalore

*August 2015 - January 2016*

Academic Qualification

**B.E** in **Electronics and Communications** in **Nandi Institute of Technology and Management Sciences** from **Visvesvaraya Technological University** with an aggregate **58.02**%, **Bangalore**

*September 2011 - January 2016*

**PUC** in **National College (Basavanagudi)** from **Karnataka Pre-University Board** with an aggregate **56.83%, Bangalore**

*June 2009 - March 2011*

**SSLC** in **Sri Gururaja Parents Teachers Association School** from **Karnataka Secondary Education Board** with an aggregate **72.96%, Bangalore**

*May 1999 - March 2009*

Technical Skills

* Strong programming skills in C Programs and Data Structures.
* Experience on low-power 8/16/32 Bit Microcontrollers 8051(AT89C51), LPC214 (ARM7 TDMIS) and LPC1788 (ARM Cortex M3).
* Design, develop, implement, test and debugging embedded system software.
* Ability to read Schematics, Data Sheet and Hardware Abstraction Layer (HAL).
* Good knowledge on Peripherals like Phase Locked Loop, Timers (Match and Capture operation), Counters, ADC, Real Time Clock and Watch Dog Timers and Interrupts (VIC).
* Fundamental knowledge of Communication Buses and Protocols like UART, I2C, SPI and CAN.
* Understanding of Real Time Operating Systems (RTOS).
* Good knowledge on Linux System programming (API Interface and IPC Mechanisms) and Device Drivers(IOCTL Commands).
* Good in Analysing and Understanding requirements.

Tools

* **Development Tools:** Keil, Flash Magic, Eclipse
* **Debugging Tools:** GDB, JTAG
* **Program Compiler:** GCC, G++
* **Editor:** VI Editor

Projects

**1) Title of the project:** Design of Applications to test the hardware features of LPC1788 Microcontroller using Hardware Abstraction Layer.

* **Peripherals used:** ADC, Uart, Timers, Real Time Clock.
* **Description:** The project was developed to test the hardware features of the LPC1788 Microcontrollers.

The following features were tested and verified:

* Using Timers to generate delay for specified time
* Counting number of clock pulses using the Counter operation
* Testing Match and Capture operations
* Transmission and Reception of data using UART
* Conversion of analog input to digital using ADC
* Testing RTC operation

**2) Title of the project:** Design and Implementation of Robotic System (Automated and Manual controlled) for detection of objects on its path.

* A Hardware application using LPC2148
* DC Motors with relay, IR Sensors, LCD, Keypad for controlling Robot

**3) Title of the project:** Design of showing three checkpoints(path) from source to destination. A Software application was developed using C Language in Linux platform.

* Addition of new routes to the file
* Modification in checkpoints for the existing route.
* Removal of existing route from the file.
* Searching and displaying checkpoints for the given source to destination.
* Displaying all existing routes in the file.

Personal Details

* **Date of Birth :** 07-01-1993
* **Father Name :** Srinivasamurthy A S
* **Mother Name :** Siddagangamma
* **Languages Known :** Kannada and English
* **Interests/Hobbies :** Playing Chess, Carrom and Cricket

Declaration

I hereby affirm that the information provided in this document is true, complete and correct to the best of my knowledge.

**Date:**

**Place: Bangalore (Narasimha N)**