

CURRICULUM VITAE

PRIYANKA SETHI

M.E.(ECE) B.E.(EE)



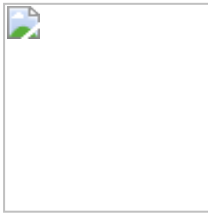
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Career Objective

To contribute a reputed organization with outstanding technical skills and obtain a position in the Department in which I can expand my skills and knowledge.

Summary

Motivated Electrical, Electronics and Communication Engineer demonstrating expertise in multidisciplinary subject areas like Digital Communication, IP and Telecom, Digital Signal Processing, Soft Computing, Embedded System design, System on Chip and Mixed Signal Analysis.

Profile

- ☒ Self-motivated professional with a passion for innovation.
- ☒ Enthusiastic learner who quickly grasps new concepts and technical skills.
- ☒ Possess ten years of experience in Research and Development field.
- ☒ 16 research papers published in reputed journals, National and International Conferences.
- ☒ Ability to handle different projects.
- ☒ Ability to read, analyze and interpret technical and scientific documents
- ☒ Possess excellent communication and listening ability
- ☒ Goal-oriented and ability to work under skills
- ☒ Design, analysis and implementation using simulation softwares.
- ☒ Software Development using C/C++ programming.
- ☒ Highly adaptable in quickly changing technical environments with strong organizational and analytical skills.

Computer Skills

Programing Language/ Simulation Softwares

Operating Systems/Software Applications

☒ MS DOS

☒ UNIX

☒ Windows XP/ 7 / 8/ 10

☒ MS Word/ Excel/ Powerpoint 2010

☒ C/ C++ , Keil C, VHDL

☒ ALTIUM 9

☒ ORCAD 16.5 (PSPICE, Capture, PCB Layout)

Familiar with LABVIEW , Proteus 8, Simulink, Arduino

Familiar with Xilinx, MATLAB

Education

☒ M.E.(ECE) in 2007 from MBM Engg College ,JNV University, Jodhpur

☒ B.E.(EE) in 2001 from MBM Engg College, JNV University, Jodhpur

☒ XII : 60.0% (Raj. Board Sec.Edu) with Chemistry, Physics, Mathematics in 1995

☒ X : 73.7% (R.B.S.E.)with Chemistry, Physics, Mathematics, English, Hindi, Sanskrit, Economics, Civics, History, Geography, Biology, SUPW and Drawing in 1993

Professional Experience

I worked as Senior Research Associate, Trainer and as a teaching faculty in various Institutions. Taught following subjects:

- Basic Analog Circuits - Electrical Engineering
- Digital Electronics - Power Electronics
- Information Theory and Coding - Computer Communication
- Mixed Signal Analysis - VHDL Programming
- PSPICE simulation - CAD for VLSI; use of Ultiboard
- Digital Communication; MATLAB simulation
- Wireless Communication; simulation using Simulink
- VLSI; simulation on Xilinx- Logic Synthesis

1.Nov 2013 to 19 May 2015 **Associate Professor**, Department of IT, Sri Guru Teg Bahadur Institute of Management and Information Technology, Delhi

2.1 Aug 2012 to 31 July 2013 **Associate Professor**, Department of Electronics Engineering, Defense Engineering College, Faculty of Engineering, Defence University, DebreZeit, Ethiopia

3.23 Nov 2011 to 31 July 2012 **Senior Project Associate**, Indian Institute of Technology Rajasthan, Jodhpur

Managed team of students who designed small modules of Company Projects.

Responsible for the software development of Low Cost Access Device project.

- i. Design and development using different Simulation software. implementation of programs of device drivers using C++
- ii. Developed a PCB layout by use of ALTIUM 9 and ORCAD software and generated the gerber files.

iii. Responsible for the design and execution of the development work as well as retaining a very hands-on involvement in the work itself.

4.1 August 2009 to 22 May 2011 **Associate Professor & Head (ECE)** MJRP University, Jaipur, Rajasthan

5. August 2007 to 31 July 2009 **Senior Lecturer (Electronics)**, Banasthali University, Newai, Rajasthan

6. Apr 2001 to May 2007 **Lecturer in Dept of EE/ CS&E & Graduate Research scholar in Dept of ECE**, MBM Engineering College, Jodhpur Rajasthan

Professional Training /Development

- ☒ Attended Nanocell webinar on "**Hetnets/ Small Cell Technology**" on 16 Feb, 2016
- ☒ Attended NI Seminars "**Basic Data Acquisition NIC-3480499**" on 14 June, 2016; "**An introduction to LabVIEW**" on 19 Apr, 2016; and "**Do Engineering- Preparing the Next Generation of System Designers**" held at Delhi on 11 July 2015
- ☒ Attended Oracle seminar on "**OpenStack on Oracle Solaris 11**" on 20 Nov, 2014
- ☒ Attended **5 Days** Faculty Development Program on "**Microcontroller based Embedded System through ICT**," organized by NITTTR Chandigarh centre at RDEC Ghaziabad, U.P. from 6-10 Jan, 2014
- ☒ Attended NI Innovation Summit 2014 at Jaipur on 12 April, 2014
- ☒ **45 days** summer course on ECONOMICS- FINANCIAL ANALYSIS in 2011
- ☒ **60 days** Course on Remote Sensing by ISRO at Banasthali University- Study the Operation of various Remote-Sensing softwares and instruments used for GIS
- ☒ Attended **7 days** course on MATLAB software, workshops on WSAD & J2EE by IBM in 2005
- ☒ Organised Engg Workshops on PSPICE, ROBOTICS, AUTOCAD and MATLAB in 2008-2010
- ☒ **10 days** Course on System Characterization Techniques using LABVIEW SOFTWARE by National Instruments in 2011
- ☒ Attended **7 Days** workshops on WSAD J2EE by IBM in 2002
- ☒ Visited Kota Thermal Power Station, ABB at Faridabad and Defense Steel Manufacturing Industry, DebreZeit, Ethiopia in 2012
- ☒ Sankhla Udyog, Jodhpur **45 days** – observed manufacturing of **Distribution and Power Transformers** in 1998
- ☒ BHEL, Hardwar **45 days** - Completed Project on **Study of Manufacturing Process of 250 MW Turbo Generator in 2000**
- ☒ 132 kV GSS at New Power House, Jodhpur **21 days in 2001**- Study and familiarization with all Electrical and Electronic meters/ Instruments/Equipments/ Protection devices/ Distribution and Transmission of Electrical Power/PLCC

Project Details

1. Project at IITJ:

Project Name: Low Cost Access-cum-Computing Device

Description: Managed a project from conceptualization to implementation

- ☒ PCB designing of tablet using ORCAD 16.5, Altium 9 & FreePCB: PSPICE level simulation and verification, Schematic level design and analysis, circuit level design and optimization, Layout design- optimization, routing and verification on ALTIUM 9 to generate the gerber files.

☒ Designing Android applications for the device

☒ Porting of Kernel on the Tablet.

2. **M.E. Seminar Topic: Sinusoidal Speech Modeling for Packet based Transmission in Digital Communication**

3. **Dissertation in M.E. Curriculum:**

Project Name: Performance Comparison of Different Speech Recovery Methods in Packet Voice Communication

Description: *The performance of various important speech recovery methods used to reconstruct missing speech packets have been compared. The purpose is to provide a good substitute waveform as fast as possible without interrupting connectivity*

4. **Final Year Project in B.E. Curriculum:**

Project Name: Economic Load Dispatch of Power System using Genetic Algorithm

Description: The principle of economic load dispatch of a power system is to determine the optimum load for various generators so that total load demand can be met within given constraints. By GA the single objective of optimum load dispatch is obtained with minimum loss. A software program in C programming language was made for the implementation.

Other Relevant Information

- Member in various groups [Embedded application professionals; http://www.researchersworld.com](http://www.researchersworld.com) / <http://www.scholarshub.net/>
- Member of The Teaching Professor group; Professor/ Lecturer group working in college at <http://www.linkedin.com/>
- Reviewer of the editorial review board of IMPACT JOURNALS. (www.impactjournals.us)
- Former member of 'Editorial Board' for International Journal of Soft Computing and Engineering (IJSCE)- www.ijscce.org
- Certification of Proficiency in English language from CIEFL, Hyderabad, India
- Topics of research interest include Optimization using Soft Computing Algorithms, Mathematical Modeling and Simulation, System on Chip, Signal Processing.
- Organised Cultural events of Extempore speech, Collage, Rangoli, sketch painting as college faculty
- Participated and earned certificates in various competitions and summer camps at school level.