

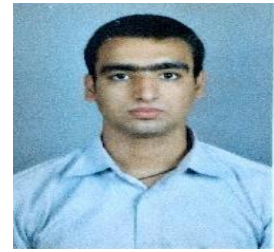
# RESUME

## **SURAJ SHENOY**

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**Objective:** To seek an Entry level position in an organization which facilitates professional growth where I can apply and improve my technical and problem solving skills to assist in the continued improvement of the organization.

### **Work experience:**

- Working as Electrical Engineer at Al Turki Enterprises.(29 September 2019- present)

#### **RESPONSIBILITIES**

- 1)Responsible for HV / LV works in installation.
- 2)Supervision of Transformers / RMU's / SMDB / DB's including cable termination works.
- 3)Supervision of external cable laying
- 4)Fixing of cable tray & trunking
- 5)Wiring and dressing of DB terminations.

- Worked as Electrical Engineer at Hindustan Unilever(23July 2018- 22 September 2019)

#### **RESPONSIBILITIES**

- 1) Maintenance and operation of 11kv substation.
- 2) Monthly energy auditing
- 3) Preventive & Breakdown maintenance works.
- 4) Routine check of electrical protection.
- 5) Operations and Maintenance of Electrical Installation.
- 6) Raising purchase order in Electrical and also consumables material of Project.

- Worked as Electrical Maintenance Engineer at MSEZL (2016 October – 22July 2018).

#### **RESPONSIBILITIES**

- 1) Maintenance and operation of 110kv and 33kv substation.
- 2) Underground cable inspection.
- 3) Supervising of streetlight repair works.
- 4) Operations and Maintenance of Electrical Installation.
- 5) Isolation and Energising of breakers.
- 6) Maintenance of Transformers and HT/LT Switchgears.

**Education:**

Sl.No.	Qualification	University	Year Of Passing	Stream	Percentage
1	B.E.	Canara Engineering College, VTU	2016	Electrical & Electronics	63.70
2	P.U.C.	SVC PU college, Karnataka PU Board	2012	Science	65.16
3	S.S.L.C.	Canara High School Karnataka State Board	2010		71.02

**Academic Projects:****Project 1:** DC Motor control using FPGA

**Description:** The main objective of this project is to control direction of rotation and speed using single controller.

Here we use FPGA technique. A (**FPGA**) is an integrated circuit designed to be configured by a customer or a designer after manufacturing, hence "field-programmable". VHDL programming is used in this project. This is open loop configuration. A set of digital signals is given to DAC by applying suitable voltage. The corresponding analog signal is obtained. This analog signal is fed to fpga to generate suitable pwm output .Accordingly DC motor speed is controlled. FPGA is programmed in such a way that after a certain value of analog signal is reached motor reverses its direction, hence direction is controlled.

**Team Size & Duration:** 3members & 3 months

**Responsibilities:** PCB design and report documentation.

**Awards & Participations:**

- Won 2nd place in National level tech fest on Crazy Ideas at CEC, Bantwal.
- Participated in National level Project Paper presentation at KVG College of Engineering ,Sullia

**Training & Certification:**

- Holder of Electrical supervisor license (class 1) by government of Karnataka.
- Attended 7 days of Soft Skill Development Programme conducted by Infosys at CEC.
- Attended 6 day Training program on Programmable Logic Controllers organized by Industrial String Automation.
- Attended Internship/Training at Karavali Switchgear, Baikampady Industrial Area. □  
Attended 7 day Internship/Training at Mangalore Electricity Supply Company Limited (MESCOM).

**Computer Skills:**

- Basic knowledge of MS OFFICE (Word, Excel & Power Point)
- AUTOCAD for electrical engineering basics
- Basic knowledge of C/C++
- Basic knowledge of PSPICE, MATLAB

**Personal Information:**

Date of Birth: 5th June 1994.

Languages Known: English, Hindi, Kannada, Tulu and Konkani.

**Declaration:**

I hereby declare that the information furnished above is true to the best of my knowledge.

Date:

Place: Mangalore

Yours sincerely,

(Suraj Shenoy)