TILAK D R TENNETI

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Summary:
With work experience in software domain and leading a startup from the front, looking forward for a challenging learning experience to shape up work skills and grow as a confident individual in domain of work, business and employment in the sector of international business.

Work Experience:

admireLadder   apr 2018 – mar 2020
CEO

  This startup was based on providing education and technology services by incorporating work skills to different cadre of individuals to meet their needs of not just performance but also growth in the reason of functioning through communication.

Sysintelli Inc
San Diego, CA
Systems Analyst   Mar 2017- Present
Network Engineer   May 2016-Feb 2017
- Responsible for mobile application design, development and maintenance on IOS platform
- Created and customized views, including table views, tab bars and navigation bars which form the basic functionality of a multi-view application.
- Experienced with working in data parsing forms such as XML and JSON to dynamically display data on iPad.
- Gained knowledge of object oriented concepts and used them in development in building re-suable code.
- Could navigate through different storyboards in UI design for short term codes
- Proficient in memory management – Manual Reference Counting(MRC) and (Automatic Reference Counting)
- Working knowledge of iOS tools (xCode, interface builder)

Infobrain Pvt Ltd
Hyderabad, India
Trainee Associate   July 2011 – May 2013
- Been part of team of debugging and unit testing
- Learned fundamentals of editor, Oracle and SQL

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- Java development using IDE
- Problem solving on day to day basis

EDUCATION:

M.S. IN ELECTRICAL ENGINEERING   Dec 2015
Missouri University of Science and Technology
Rolla, Missouri

B.E. IN ELECTRICAL AND ELECTRONICS ENGINEERING   Apr 2012
Gandhi institute of technology and management
Visakhapatnam, A.P, India

Relevant Coursework: Wireless Communications, Digital Logic, Fault Tolerant Digital Systems, Real Time Digital Signal Processing Lab, Digital System Design lab, Embedded System Processor Design Lab, Neural Networks for Control, Optical Computing, Digital Image Processing, Advanced Digital Signal Processing, Machine Vision, Programming with C/C++

Technical skills:

Programming languages: C, C++, Verilog, VHDL, ARM Assembly, Objective C, Java, Swift, HTML, CSS, XML, Json, MySQL
Tools: MATLAB, pSpice, xCode
Wireless Protocols/Technologies: LTE, WCDMA, OFDM, MIMO, CA

Academic Project Experience:

- **LTE Technical Review:**
  - o Reviewed 4G Wireless Communication Protocol LTE and its physical and mac layer implementation.
  - o Analyzed the technologies including MIMO, OFDM and Carrier Aggregation (CA) at system level.
- **Wireless Communications Lab:**
  - o Designed and implemented large scale fading and small scale fading channels
  - o Designed and implemented transmitter and receiver diversity, Linear and MMSE equalization and convolutional coding techniques in MATLAB
- **Diagnosis of skin cancer by detection of keratin plugs algorithm:**
  - o After analyzing the images, different approach methods were tried to identify seborrheic keratosis and thus distinguish it from melanoma
- **Designed Kalman filter algorithm for echo cancellation**
  - o Implemented Kalman filter algorithm by estimating a set of unknown variables acquired over noisy observations obtained by the microphone which has echo path corrupted by Gaussian noise.

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- **Designed Generalised Sidelobe Canceller (GSC) based beamformer**
  o Implemented adaptive weight vector for lower branch channels to estimate interference in the upper branch.
- **Designed cancer detection algorithm using Adaptive Resonance Theory(ART) in MATLAB**
  o Implemented Adaptive Resonance Neural Network with a constraint of vigilance parameter to diagnose cancer
- **ARM Assembly Programming Lab**
  o Implemented serial communication using USART (universal synchronous/asynchronous receiver transmitter) and TWI (two wire interface) and set the parameters for timers to generate pulse width modulated waveforms
- **Embedded C Programming Lab**
  o Built basic 8051 microcontroller circuit with auto-ISP circuit and initialized the UART
  o Set up the SPI peripheral and initialized the SD card
  o Developed a file system that allows the microcontroller to find and read an mp3 file from the SD card
  o Developed routines needed for inter integrated circuit (i2c) or two wire interface (TWI) and used those functions to initialize STA013MP3 Decoder.
- **Implemented APA and PNLMS algorithm:**
  o Designed affine projection algorithm to reduce the adaptive noise cancellation and proportional normalized least mean squares to improve the adaptive convergence rate
- **Implemented MIPS classic five stage pipeline architecture:**
  o Designed microprocessor without interlocked pipeline stage architecture consisting of five stages: fetch, decode, and execute, memory and write-back.