**RESUME**

**SHEIK ASIF**

**ELECTRICAL ENGINEER|ELECTRICAL DESIGN AND DRAFTING.**

Mobile : +918500130844

Email : aasif295@gmail.com

**CAREER OBJECTIVE**

A dynamic professional seeking a position to utilize skills and abilities in the environment that offers professional growth while being resourceful, innovative and flexible. Willing to work as a key player in challenging and creative environment.

* A budding professional Bachelor of Engineering Electrical Engineering.
* An effective communicator with exceptional strong interpersonal skills and in-depth and intense experience in the entire gamut of databases.
* Excellent analytical, troubleshooting & inter-personal skills with proven ability in driving numerous quality enhancement, process improvement & cost savings initiatives during career span.
* Eager to contribute in a team-oriented environment having inner desire to gel in cohesive team and work for business growth.
* Punctual
* Quick Learner
* Leader
* Responsible

I am good at interpersonal, communication and organizational skills with proven abilities in enhancing, optimizing resources & capacity utilization, improving, resolving problems, ramping up the activities with on time deliverables and maximizing efficiency.

I am committed to Ethics, Integrity, Loyalty and commitment to the organization.

**ACADEMIC QUALIFICATION**

**Bachelor of Technology (B-TECH) |Electrical & Electronics Engineering.**

* **Institute:**Audisankara Institute Of Technology, Gudur, Andhra Pradesh, India.
* **Results:**Achieved **66 %** overall results.
* **Project Title:** Design And Implementation of A New Multilevel Inverter Topology.

**Board of Intermediate(MPC)**

* **Institute:**Narayana junior college, Nellore ,Andhra Pradesh, India.
* **Results:**Achieved A grade with **78.2%** overall results.

**Secondary school of education(SSC)**

* **Institute:** Sri Sai Durga English Medium high school, Narukuru Village, AndhraPradesh, India.
* **Results:**Achieved A grade with **65%** overall results.

**PROJECT DETAILS**

Project Title ::- Design And Implementation Of A New Multilevel Inverter Topology.

* Studied well about Multilevel inverters and it's classification.
* In this project , a new topology with a reversing voltage component is proposed to improve the multilevel performance.
* In our project we design a seven level inverter and simulated successfully by using MATLAB simulation.

**WORK EXPERIENCE**

* Working as a Commissioning Engineer cum Site Engineer from October-2015 to October-2017 in General Electricals and Refrigeration.
* Working as a Technical Engineer in Airports Authority Of India under Contract base From Jan-2018 To June - 2019.
* Working as Project Engineer in Jazzcon Engineers Pvt Ltd From October – 2019 To Till.

**PROFESSIONAL TRAINING**

**JULY 2015 – AUG 2015 KG MECH Electro Mechanical PVT LTD.**

 **“Electrical - Design and Drafting Engineer”**

**I can work in this environment**

* Design
* Drafting
* Site
* QA/QC
* Maintanence

Electrical Design And Drafting Details:

✔ Codes & Standards ✔ Load Calculation ✔ Transformer Sizing

✔ Diesel Generator Sizing ✔ Cable Sizing ✔ UPS & Battery Sizing

✔Drawing Detailing ✔AutoCAD Implementation ✔ Preparing Sections

✔ Earthing Calculation ✔ Cable Tray Calculation ✔ Light Features

✔ Television ✔ Sensor System ✔ Detectors System

 **Designing Work Ability – Electrical:-**

* Providing proper Lighting System by using DIALUX Software
* Maximum demand estimation and deciding voltage level.
* Selection of transformer/Dg capacity and number with necessary metering and protection.
* Preparation of detail main single line schematic.
* Short circuit & voltage drop calculation at each level of distribution.
* Selection of HT/LT switches gear based on breaking, making &short time capacity.
* Selection of HT/LT cables based on thermal, short circuit & voltage drop based capacity.
* Preparation GA/sectional drawings, HT & LT switch board and busbar sizing.
* Design of Earthing & lighting protection system.
* Design of fire-safety in substation, cable trenches & other parts of electrical system.
* Preparation of power & control cable schedule.

**Design Software:**

* AutoCAD - Expert Level ,MS – Excel, PDF – Professional, Master Convertor,
* Dialux software
* Dialux Evo
* ETAB
* Electrical Maintanence

**PROJECTS**

**1.Projects Handled:-**

**Project Name** :- Arch Saudi Project.

**Main Contractor** :- KG Mech Electromechanical pvt ltd.

**Project Details** :- Design and Installation of Lighting system by using DIALUX software and all other loads and electrical accessories for G+5 building.

**2.Project Name** :- Qatar Project.

**Main Contractor :-** KG Mech Electromechanical pvt ltd.

**Project Details** :- Design and Installation of Lighting system by using DIALUX software and all other loads and electrical accessories for G+4 Commercial shopping mall.

**EXTRA CURRICULAR ACTIVITIES**

* Coordinator of a social welfare organization named **“MAHAYAGA HELPING SOCIETY”.**
* Organized various Social & Health camps.
* Took active part in marathon speeches on**” Ethical Voting”** which got enrolled in **“Limca book of World Records”**, which is Specially appreciated by honorable District Collector “**Sri N.Srikanth Garu”.**

**ATTRIBUTES AND STRENGTHS**

* Dedication, amicably motivate in a team, self-motivation, positive attitude and confidence to achieve goals.
* Willingness to learn and contribute to the growth of organization.
* Ability to intermingle in a group and adaptive to any kind of situation.

**FUNCTIONAL SKILLS**



|  |  |
| --- | --- |
| **Software :** | Microsoft Office Suite. |
| **Languages :** | C. |
| **Operating Systems :** | Windows 2007/2008. |

**PERSONAL VITAE**

Name : SHEIK ASIF

Father’s name : SK CHAND BASHA

Mother’s name : SK RAHAMATH

Date of Birth : 1oth March, 1994

Gender : Male

Marital status : Single

Nationality : Indian

Address : Narukuru Village And Post ;

 Thota Palli Gudur Mandal ;

 Nellore (Dt) ;

 Andhra Pradesh ;

 India ; 524002.

Languages : English, Hindi, Urdu and Telugu.

**DECLARATION**

 ***I hereby declare that the above-mentioned information is correct up to my Knowledge and I bear the responsibility for the correctness of the above-mentioned particulars.***

 Date : **(ASIF SHEIK)**

 Place :